ANALOG IC DESIGN ENGINEER RESUME

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Career Goal:

To showcase my talent as an Analog IC Design Engineer and contribute my skills in CMOS, circuit design, BJT, MOS and Power devices.

Technical Skills:

Sound technical knowledge of discrete electronic design including:

- BJT
- MOS
- Power devices

Thorough with programming languages:

- Fortran, C
- Basic, Pascal
- Turbo

Proficient in utilizing analog and mixed-signal simulators:

- Spice, Spectre
- UltraSim
- amsSpectre

Familiarity with connectivity driven layout tools like:

- VXL, VCP
- VCR
- CCAR

Adept with MATLAB script development to facilitate:

- IC Design
- Chip laboratory test
- Data visual plotting

High competence to utilize IC layout verification tools like:

- Diva
- Calibre/ xCalibre
- DRC, LVS, PEX

Crystal clear with cellular standards including:

- CDMA2000
- WCDMA, IX-EVDO
- UMTS, GMS/ GPRS

Immense capabilities to:

- Utilize design tools like Cadence
- Implement analog circuit theory

Relevant Experience:

Analog IC Design Engineer
Linear Technology Corporation
January 2012 – Present
Houstonia, MO

- Prepared architectural structures, designs along with verification of circuits, logic and algorithms.
- Provided technical expertise for implementation of concepts in new circuit topologies.
- Assisted in specification and designing of CMOS and BCD power management circuit systems.
- Conducted theoretical analysis to determine effective design approaches and parameters.
- Implemented procedures for verification of designs through use of simulation tools.
- Coordinated with layout designers for designing and development of custom layouts for systems.

- Evaluated designs results utilizing laboratory measurements of ICs and related techniques.
- Participated in technical design reviews for providing new inputs and learning from other designers.

Analog IC Design Engineer
Samsung Information Systems America, Inc. (SISA)
November 2010 – January 2012
San Jose, CA

- Developed circuit architecture systems and related topologies to support performance of circuits.
- Provided technical assistance for development of specifications for various analog blocks.
- Implemented procedures for optimization of circuits through simulation tools for all process and operating functions.
- Supported technical team members in characterization and implementation of ASICs.
- Executed processes for integration of circuits for analog and mixed signal ASICs.
- Assisted in specification, architectural development, layout supervision and lab validation procedures.
- Coordinated with product and application engineering teams to meet production and manufacturing requirements.
- Maintained technical documentation for designs and layouts as per MCU designing standards.

Educational Background:

Bachelor's Degree in Electronic Engineering
Los Angeles Trade Technical College
August 2006 – May 2010
Los Angeles, CA

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