FPGA ENGINEER RESUME

Objective:

To take a challenging role as FPGA Engineer in a highly technical company and utilize my skills in MATLAB, C++ applications, Xilinx ISE, and ModelSim in providing efficient and effective service to the company in attaining its goals.

Education:

PHD degree in Microelectronics
Yale University

Master of Microelectronics, 2002
Yale University

B.S Electronics, 1999
Yale University

Skills:

- Great exposure and knowledge in Communication theory and Digital Signal Processing
- MATLAB and C++ application use and simulation of algorithms
- FPGA implementation of systems.C, C++ applications
- Knowledge in Eclipse IDE, Xilinx ISE, ModelSim, MATLAB Simulink Packages
- Exposure in JAVA, AHDL, Assembly language (8086)

Professional Experience:

- FPGA Engineer III, 2008-present
- Halex Corp., Georgetown, MA

Responsibilities:

- Analyzed and optimized changes in detection sector network.
- Researched scheme for MAC delays mitigation.
- Performed different simulation activities to reveal the importance of cross layer optimization.

- FPGA Engineer II, 2005-2008
- Halex Corp., Georgetown, MA

Responsibilities:

- Evaluated the operational procedures in 802.11a/g, 802.16e Cognitive Radio platform development.
- Implemented FPGA to be able to generate data frames.
- Supervised the BFM Simulation transmitter and receiver.

- FPGA Engineer I, 2002-2005
- Halex Corp., Georgetown, MA

Responsibilities:

- Analyzed and optimized changes in detection sector network.
- Researched scheme for MAC delays mitigation.
- Performed different simulation activities to reveal the importance of cross layer optimization.

- FPGA Engineer II, 2005-2008
- Halex Corp., Georgetown, MA

Certifications and Affiliations:

MicroStrategy Certified Engineer
Association FPGA Consultants in Georgetown

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