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# FPGA PROGRAMMER RESUME

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## Career Goal:

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To further enhance my technical calibers in Verilog, Xilinx, FPGA, Altera, DSP and Matlab as a FPGA programmer and thereby lead to organizational success.

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## Technical Skills:

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Operational competence to implement digital design using:

- VHDL
- Verilog
- System Verilog

Adept in utilizing tools like:

- ModelSim
- Questa, ISE
- Design Compiler

Thorough with source management software:

- Clear case
- SVN, CVS
- Git

Brilliant in utilizing programming languages:

- C, C++, C#
- Java, Python
- JSON, HTML

Solid understanding of environments like:

- Unix, Linux
- Windows
- Cygwin

Adroit in utilizing modulators like:

- FEC, upsampling
- Pre-equalization
- D to A conversion

Crystal clear with random processes:

- Gaussian channels
- Noise generation
- SNR calculations

Immense capabilities to:

- Utilize Xilinx system generator and Xilinx tools
- Participate in real time embedded software development

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## Relevant Experience:

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FPGA Programmer  
Praxis, Inc.  
January 2012 – Present  
Mesa, AZ

- Designed, developed and modified code, Xilinx ISE and digital circuits.
- Executed program code on Modelsim and Lab VIEW platforms.
- Conducted technical design reviews and requirements negotiation.
- Participated in learning of fabrication and testing methods of circuit boards.
- Prepared technical documentation and designed testing suites.
- Created software and embedded firmware systems for electronic equipments.

- Utilized VHDL applications for development of FPGA code.
- Coordinated with engineers for development of DSP and DFRM systems

FPGA Programmer  
EOIR Technologies  
November 2010 – January 2012  
Dahlgren, VA

- Formulated functional specifications and architectural designs.
- Analyzed bandwidth and system interfaces with DDR programs.
- Executed HDL coding and designed scaling of video formats.
- Supported filtering and designing of functions with FPGA resources.
- Conducted synthesis and timing analysis for validation of performance.
- Designed and developed test matrix and functional test activities.
- Created technical designs and IP for DVI standards.
- Installed and maintained communication conference and PTT security module.

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### **Educational Background:**

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Bachelor's Degree in Computer Science  
Johnson State College  
August 2006 – May 2010  
Johnson, VT

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