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# RFIC DESIGNER RESUME

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## Career Goal:

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Seeking the position of RFIC Designer to share my expertise in VHDL, Verilog and OCEAN technologies.

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## Technical Skills:

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Expertise in CAD tools:

- Synopsis DC
- DFI
- PowerMill

Specialist in scripting technologies:

- UNIX
- Shell
- AWK

Quick and excellent in communication systems:

- MAC
- DSP
- GPRS

Dexterity in IC designing software:

- Cadence
- Mentor graphics
- Xilinx

Solid understanding of instruments:

- BT tester
- Network analyzer
- Channel simulator

Adept in demodulation techniques of:

- FSK
- BPSK
- MSK

Familiarity with simulation tools:

- Agilent ADS
- Genesys
- EMPro

Immense abilities to:

- Prepare eye diagrams and implement semi analytic simulation
- Execute demodulation and use channel modeling techniques

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## Relevant Experience:

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RFIC Designer  
Qualcomm Inc  
January 2012 – Present  
Bowling Green, KY

- Designed and developed RFIC blocks utilizing Cadence tools.
- Formulated and executed transceiver specifications for problem resolution.
- Prepared circuit level specifications and supported digital loops.
- Created and implemented calibration algorithms including phase mismatch.
- Conducted linear and noise analysis for simulation purpose.
- Developed physical layouts containing all circuits as per layout requirements.
- Provided assistance in design reviews and SOC verification activities.

- Utilized CMOS technologies for administering RF transceiver products.

RFIC Designer  
Skyworks Solutions  
November 2010 – January 2012  
Woburn, MA

- Supported laboratory development and project management of clients.
- Participated in testing operations utilizing FOCUS and APCR measurements.
- Assisted in understanding of calibration techniques utilizing connected solutions.
- Designed board layout and prepared package designs.
- Provided socket designs for Agilent and other testers.
- Created test plans and RF circuit architecture systems.
- Implemented IC assembly activities and lab evaluation.
- Prepared project documentation such as circuit and general specifications.

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### **Educational Background:**

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Bachelor's Degree in Information Technology  
Reid State Technical College  
August 2006 – May 2010  
Evergreen, AL

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