VALIDATION ENGINEER RESUME

Objective:

To obtain a Validation Engineer in an established company and utilize my technical skills in Computer Languages, Hardware, Operating System and Software applications and make a positive contribution to the company.

Education:

MS Computer Science, 2003 University of Arizona

BS in Computer Science, 1999 University of Arizona

Skills:

- C, C++, TCP/IP, COBOL, JAVA
- · Sun, UNIX Server, Dell
- Windows NT/2000/2003, UNIX
- · Oracle, SQL Server, IMS
- Experienced with CIP & SIP Processes
- Strong communication skills

Professional Experience:

- Sr. Validation Engineer, 2008-present
- H.A Stiles Company, Alpine, CA

Responsibilities:

- Prepared scheduling and planning for engineering activities using TCCP.
- Proposed new technologies and information based on objective reviews and updates.
- Evaluated engineering expenditures based on proposed budget and schedule.
- Ensured that design objectives are achieved by verifying engineering design releases.
- Jr. Validation Engineer, 2005-2008
- · Haber Inc., Alpine, CA

Responsibilities:

- Monitored the flow and scheduling of various engineering programs such as tooling, pilot, and design releases.
- Tested and verified quality of hardware and software components.
- Analyzed ands repaired defective procedures to save costs on repair.
- Compared compatibility of OS with debugged applications.
- Validation Engineer, 2003-2005
- · Hager Companies, Alpine, CA

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- Haber Inc., Alpine, CA

Certifications and Affiliations:

Certificate in System Design Engineering Association of Certified Engineers

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