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## Verification Engineer Resume

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### Job Objective

Verification Engineer seeking position with challenging organization needing dedicated and enthusiastic employees.

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### Summary of Qualifications:

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- Remarkable experience in logic design and verification as well as System Verilog Test Bench development
  - Outstanding knowledge of Verilog and verification methodology
  - Proficient in functional verification of complicated designs
  - Familiarity with SystemVerilog, OVM/VMM, Memory Stick, MMC, ASIC products, OVM, VHDL, HDL, PLI and DPI
  - Amazing capability to handle multiple tasks and meet timelines
  - Immense capability to manage autonomously and in a team
  - Solid communication skills
  - Superior problem solving programming skills
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### Work Experience:

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Verification Engineer, August 2005 – Present  
Xilinx, New Haven, CT

- Advanced coverage driven verification methodologies.
- Implemented simulation.
- Established functional coverage and coverage analysis environment.
- Analyzed industry leading tools for use in design verification.

Verification Engineer, May 2000 – July 2005  
LSI Logic Corporation, New Haven, CT

- Advanced System Verilog Test Bench.
  - Managed full-chip verification on Flash memory projects.
  - Handled System Verilog Assertions and behavioral modeling in SystemC and C++.
  - Supported design and verification methodology enhancements.
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### Education:

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Bachelor's Degree in Computer Engineering, Wayne State College, Wayne, NE

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