VHDL PROGRAMMER RESUME

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Career Goal:

To join your prestigious organization as a VHDL Programmer and help in organizational growth using my knowledge in VHDL, Altium, Xilinx, Power point, Word and Excel.

Technical Skills:

In depth knowledge of hardware components including:

- FPGA Design
- Analog/ Digital/ Mixed Signal Design
- Embedded systems

Thorough with programming languages:

- VHDL, C#
- C, C++
- Assembly

Superb in utilizing software tools:

- Xilinx ISE
- Matlab, Quartus
- Modelsim

Proficient in using other tools namely:

- LabView
- Satellite tool kit
- Code vision

Adroit with software applications:

- Microwave Office
- CST, HFSS
- PSpice, Win CE

Technically sound in utilizing various types of circuits:

- Digital
- Analog
- RF

Well acquainted with Microsoft applications:

- Word
- Excel, Visio
- Power point

Sound capabilities to:

- Perform circuit board level soldering
- Manage firmware development for Xilinx products

Relevant Experience:

VHDL Programmer
EOIR Technologies
January 2012 – Present
Rochester, NY

- Designed and developed current VHDL codebases with FPGA utilization.
- Created and optimized current application codebases.
- Prepared VHDL designs and formulated coding and configuration management procedures.
- Implemented software requirements and proposed algorithms for system functionality.
- Maintained system baseline documentation and system operation methods.
- Participated in reviews of specifications, test benches and technical reports.

- Conducted system, unit and integration testing for VHDL products.
- Provided technical assistance for programming of algorithms for VHDL syntheses systems.

VHDL Programmer
Physical Optics Corporation
November 2010 – January 2012
Torrance, CA

- Created and modified control boards for various programs.
- Developed corresponding firmware components and designed diesel locomotives.
- Created computerized boiler control systems for multiple factories.
- Supported GCS projects and programmed robot designs.
- Implemented prototyping functions and developed FPGA components.
- Prepared and maintained functional database applications.
- Participated in verification of compliance processes of all VHDL products.
- Designed and updated algorithms for partitioning of system levels.

Educational Background:

Bachelor's Degree in Computer Science
Fielding Graduate University
August 2006 – May 2010
Santa Barbara, CA

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